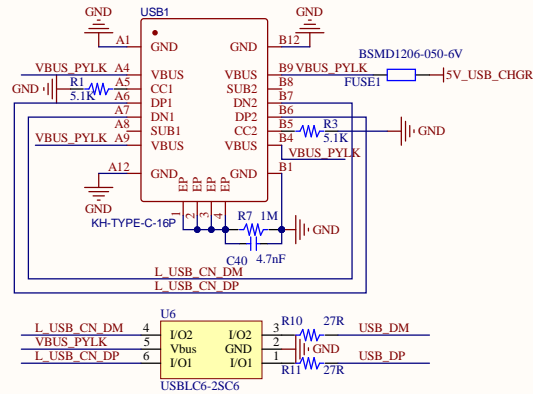
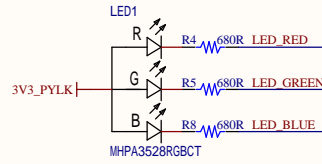


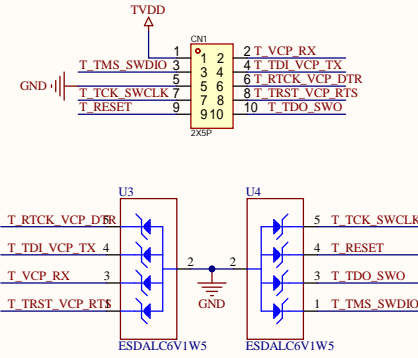
USB



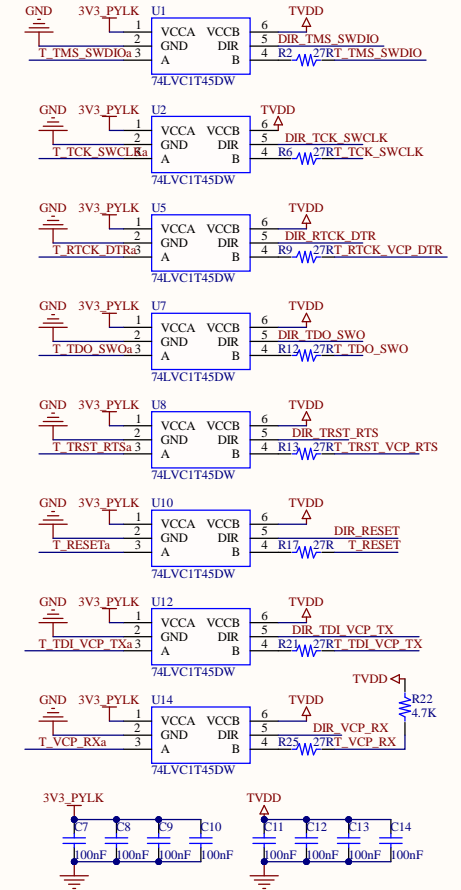
LED



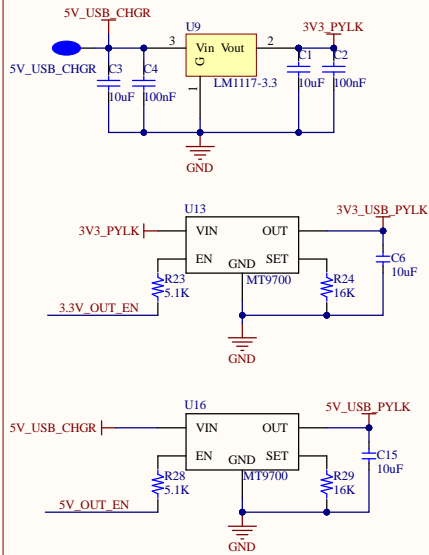
Debug Connector



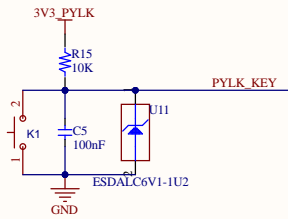
Level Shift



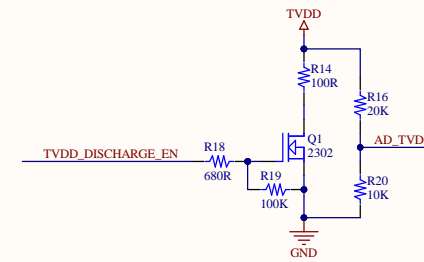
Power



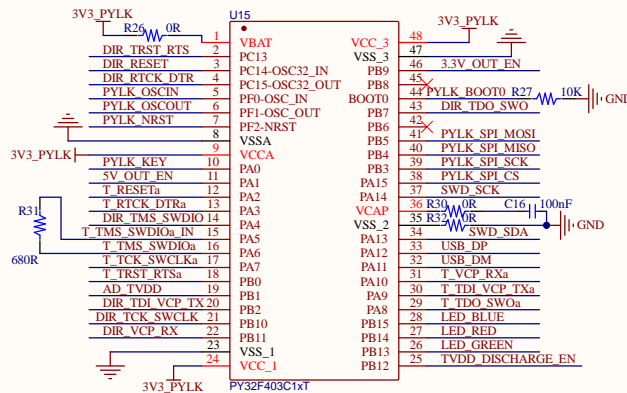
Key



ADC&Electric discharge



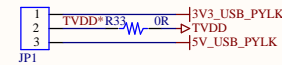
MCU



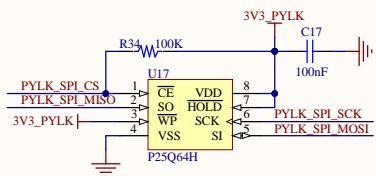
SWD



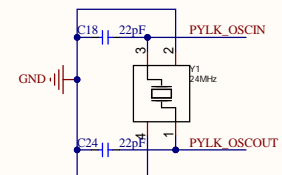
TVDD_Selection



Flash

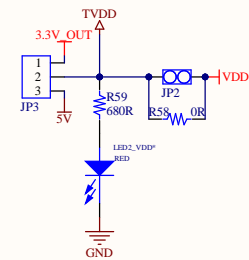


OSC

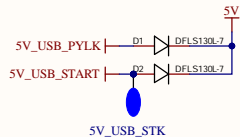
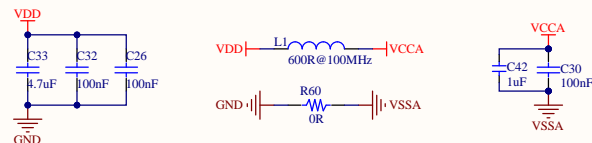
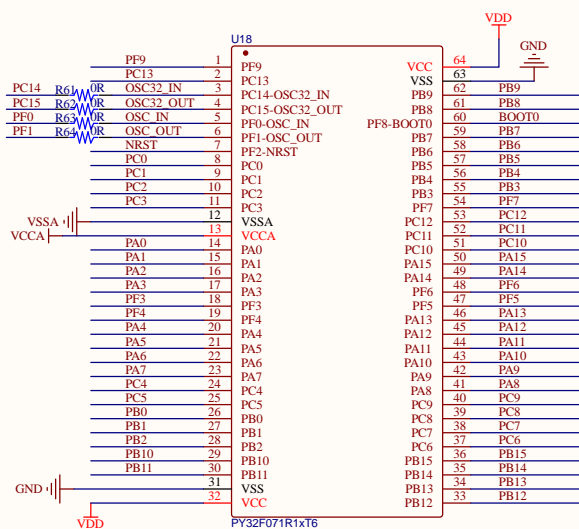


VDD_Selection

The diagram shows a circuit for selecting the VDD supply. A 3.3V_OUT pin from a connector JP3 is connected to a node. This node is connected to a 680R resistor (R59) which is connected to TVDD. It is also connected to a 0R resistor (R58) which is connected to VDD. A red LED (LED1_VDDP) is connected from this node to GND. A connector JP2 is connected between the node and VDD.

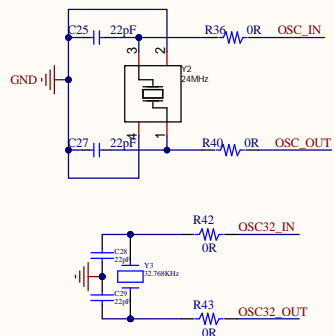


The diagram illustrates the 5V_PWR_Selection circuit. It features two input signals, 5V_USB_PYLK and 5V_USB_START, which are connected to the inputs of two 74VHC125 octal monostable multimers, labeled D1 and D2. The outputs of D1 and D2 are connected to a 5V supply through a 10k resistor. The output of D2 is also connected to the 5V_USB_STK signal.

[illegible]

OSC

The top diagram illustrates an active probe circuit. It features a 22pF capacitor (C25) connected to the probe tip, a 22pF capacitor (C27) connected to ground, and a 2MΩ resistor (R36) connected to the probe tip. The bottom diagram illustrates a passive probe circuit. It features a 22pF capacitor (C28) connected to the probe tip, a 22pF capacitor (C29) connected to ground, and a 2MΩ resistor (R42) connected to the probe tip. Both diagrams include a ground symbol and a 2MΩ resistor (R40) connected to the probe tip.



Extension PIN

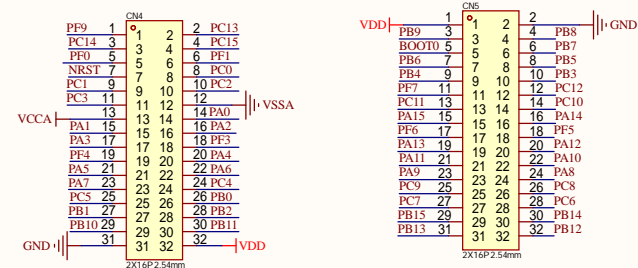
Pin 1-32:

Pin	Function	Pin	Function
1	PF9	17	PF4
2	PC14	18	PA5
3	PF0	19	PA7
4	NRST	20	PCS
5	PC1	21	PC7
6	PC3	22	PB15
7	VCCA	23	PB13
8	PA1	24	CDS
9	PA3	25	B0
10	PF6	26	B1
11	PC11	27	B2
12	PA15	28	B3
13	PF7	29	B4
14	PC13	30	B5
15	PA0	31	B6
16	PF3	32	B7

Pin 33-64:

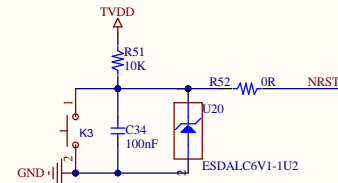
Pin	Function	Pin	Function
33	VDD	49	B10
34	PB9	50	B11
35	BOOT0	51	B12
36	PB6	52	B13
37	PB4	53	B14
38	PF7	54	B15
39	PC11	55	CDS
40	PA15	56	B0
41	PF6	57	B1
42	PA13	58	B2
43	PA11	59	B3
44	PA9	60	B4
45	PCS	61	B5
46	PC7	62	B6
47	PB15	63	B7
48	PB13	64	B8

2X16P2 54mm



NRST

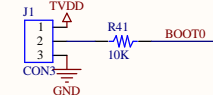
The diagram shows the NRST pin connected to a 0R resistor (R52) and a 10K resistor (R51) to TVDD. It is also connected to a 100nF capacitor (C34) to GND and a diode (U20) to GND. The diode is labeled ESDALC6V1-1U2.



LED



BOOT_SEL



USB & POWER

